

REMARKS

In light of the remarks to follow, reconsideration and allowance of this application is respectfully requested.

Claims 1-7 are in this application.

Claims 1-7 are rejected under 35 U.S.C. §103(a) as being unpatentable over U.S. Patent No. 6,470,497 to Ellis.

Independent claim 1 recites in part as follows:

“...writing means for writing to said schedule list said preset information established by said establishing means ...”

In explaining the above 103 rejection, the Examiner appears to rely on col. 5, lines 61-65 of Ellis and the micro-controller 16 and a program circuit 21 recited therein of Ellis to teach the writing means of claim 1. It is respectfully submitted that Ellis as applied by the Examiner (hereinafter “Ellis”) does not teach the above recited feature. That is, although Ellis may write data to an EEPROM 20 using micro-controller 16 and a program circuit 21, Ellis does not appear to teach that preset information established by an establishing means is written to a schedule list by the writing means. Accordingly, claim 1 is believed to be distinguishable over Ellis.

For reasons similar to those described above with regard to claim 1, independent claims 5-7 are believed to be distinguishable over Ellis.

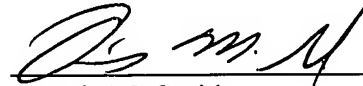
Claims 2-4 depend from claim 1, and, due to such dependency, are believed to be distinguishable over Ellis for at least the reasons previously described.

In the event that the Examiner disagrees with any of the foregoing comments concerning the disclosures in the cited prior art, it is requested that the Examiner indicate where in the reference, there is the bases for a contrary view.

In view of the foregoing remarks, it is believed that all of the claims in this application are patentable over the prior art, and early and favorable consideration thereof is solicited.

Respectfully submitted,
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